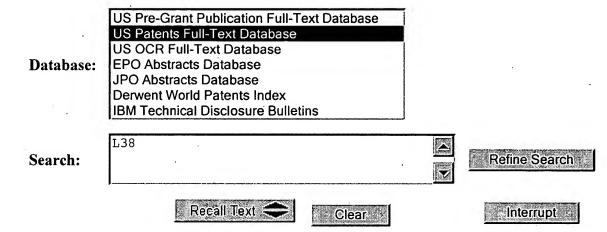# Refine Search

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Terms	Documents
L19 and (confirm$ or determin$ or check$ or identi$) same (upgrad$ or updat$ or modi$ or reprogram$)	1

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DB=USPT; PLUR=YES; OP=ADJ			
L38	l19 and (confirm$ or determin$ or check$ or identi$) same (upgrad$ or updat$ or modi$ or reprogram$)	1	L38
L37	L36 and re$	1	L37
L36	l19 and (initial$ or start$ or boot$ or signal$)	1	L36
L35	l19 and (shift$ or switch$ or on$ or off$)	1	L35
L34	l19 and (AC or alternat$ or power$)	1	L34
L33	l32 and (power$ near9 fail$)	1	L33
L32	l19 and (remov$ or delet$ or power$ or off$)	1	L32
L31	l19 and user$	1	L31
L30	l19 and (blank$ or eras$ or delet$ or remov$)	1	L30
L29	l19 and (updat$ or upgrad$ or modif$ Or alter or chang$ or reprogram$)	1	L29
L28	l19 and (cop$ or load$ Or download$) near9 ram$	1	L28

L27	l19 and (determin$ or select$ or check$) same control$	1	L27
L26	l19 and (reprogram$ or delet$ or replac$ or updat$ Or upgrad$)	1	L26
L25	l19 and (processor$ or control$ or micro$)	1	L25
L24	l19 and post$	1	L24
L23	l19 and (recover$)	1	L23
L22	l19 and (updat$ or upgrad$ or modif$ Or alter or chang$ or reprogram$) near4 (ROM or BIOS)	1	L22
L21	l19 and (updat$ or upgrad$ or modif$ Or alter or chang$) near4 (ROM or BIOS)	1	L21
L20	L19 and first memory and second memory	0	L20
L19	6442067.pn.	1	L19
L18	L17 and (updat$ or modif$ or upgrad$ or chang$ or alter) near5 (memory$ or bios$ or flash$ or Rom$)	67	L18
L17	L13 and l9	109	L17
L16	L13 and l10	0	L16
L15	L13 and l11	0	L15
L14	L13 and l12	0	L14
L13	717/168-178.ccls.	1188	L13
L12	L11 and (updat$ Or modif$ Or upgrad$) near5 (memory$ Or bios$ or flash$ or rom$)	39	L12
L11	L10 and ((power on self$) or post$)	46	L11
L10	L9 and (rom$ near5 (two$ near4 (block$ Or div$ or separat$)))	168	L10
L9	L8 and rom	20464	L9
L8	(first near4 memory$) and (second$ near4 memory$)	68239	L8
L7	L6 and (STB or test$)	1	L7
L6	l4 and power$	1	L6
L5	l4 and post$	0	L5
L4	5960445.pn.	1	L4
L3	4960445.pn.	1	L3
L2	6984590.pn.	1	L2
L1	5784317.pn.	1	L1

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Search Results -

Terms	Documents
(713/2).ccls.	1137

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Search:

L12

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L12	713/2.ccls.	1137	L12
L11	700/81.ccls.	120	L11
L10	L9 and l8	3	L10
L9	first memory and second memory and rom and ram and (update$ or upgrad$ or modif$) near4 memory and POST	156	L9
L8	711/102-104.ccls.	1382	L8
DB=TDBD; PLUR=YES; OP=ADJ			
L7	first memory and second memory and rom and ram and (update$ or upgrad$ or modif$) near4 memory and POST	0	L7
DB=DWPI; PLUR=YES; OP=ADJ			
L6	first memory and second memory and rom and ram and (update$ or upgrad$ or modif$) near4 memory and POST	0	L6
DB=JPAB; PLUR=YES; OP=ADJ			

<u>L5</u>	first memory and second memory and rom and ram and (update$ or upgrad$ or modif$) near4 memory and POST	0	<u>L5</u>

DB=EPAB; PLUR=YES; OP=ADJ

<u>L4</u>	first memory and second memory and rom and ram and (update$ or upgrad$ or modif$) near4 memory and POST	0	<u>L4</u>

DB=USOC; PLUR=YES; OP=ADJ

<u>L3</u>	first memory and second memory and rom and ram and (update$ or upgrad$ or modif$) near4 memory and POST	1	<u>L3</u>

DB=PGPB; PLUR=YES; OP=ADJ

<u>L2</u>	first memory and second memory and rom and ram and (update$ or upgrad$ or modif$) near4 memory and POST	69	<u>L2</u>
<u>L1</u>	first memory and second memory and rom and ram and (update$ or upgrad$ or modif$) near4 memory	503	<u>L1</u>

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1 Accelerating shared virtual memory via general-purpose network interface support ⬛

Angelos Bilas, Dongming Jiang, Jaswinder Pal Singh
February 2001 **ACM Transactions on Computer Systems (TOCS)**, Volume 19 Issue 1
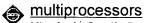**Publisher:** ACM Press

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Clusters of symmetric multiprocessors (SMPs) are important platforms for high-performance computing. With the success of hardware cache-coherent distributed shared memory (DSM), a lot of effort has also been made to support the coherent shared-address-space programming model in software on clusters. Much research has been done in fast communication on clusters and in protocols for supporting software shared memory across them. However, the performance of software virtual memory (SVM) is sti ...

Keywords: applications, clusters, shared virtual memory, system area networks

2 Cellular disco: resource management using virtual clusters on shared-memory multiprocessors ⬛

Kinshuk Govil, Dan Teodosiu, Yongqiang Huang, Mendel Rosenblum
August 2000 **ACM Transactions on Computer Systems (TOCS)**, Volume 18 Issue 3
Publisher: ACM Press

Full text available: 📄 pdf(287.05 KB)   Additional Information: full citation, abstract, references, citings, index terms, review

Despite the fact that large-scale shared-memory multiprocessors have been commercially available for several years, system software that fully utilizes all their features is still not available, mostly due to the complexity and cost of making the required changes to the operating system. A recently proposed approach, called Disco, substantially reduces this development cost by using a virtual machine monitor that laverages the existing operating system technology. In this paper we present a ...

Keywords: fault containment, resource managment, scalable multiprocessors, virtual machines

3 ⬛

Cellular Disco: resource management using virtual clusters on shared-memory

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